

45/6
6-19-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
CORONEL ET AL.

Serial No. Not Yet Assigned

Filing Date: Herewith

For: DRAM MEMORY INTEGRATION
METHOD

I HEREBY CERTIFY THIS PAPER OR FEE IS BEING
DEPOSITED WITH THE U.S. POSTAL SERVICE
"EXPRESS MAIL POST OFFICE TO ADDRESSEE"
SERVICE UNDER 37 CFR 1.10 ON THE DATE
INDICATED BELOW AND IS ADDRESSED TO:
BOX PATENT APPLICATIONS, ASSISTANT
COMMISSIONER FOR PATENTS, WASHINGTON,
D.C. 20231.

EXPRESS MAIL NO: EL747059793US

DATE OF DEPOSIT: January 9, 2002

NAME: Dawn Kimler

SIGNATURE: Dawn Kimler

PRELIMINARY AMENDMENT

Director, U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Prior to the calculation of fees and examination of
the present application, please enter the amendments and
remarks set out below.

In the Claims:

Please cancel Claims 1 to 10.

Please add new Claims 11 to 37.

11. A method for making a dynamic random access
memory (DRAM) comprising a plurality of memory cells, each
memory cell connected to a bit line and a word line and
comprising a storage capacitor and an access transistor, the
method comprising:

forming a barrier layer on a substrate, and forming
a first dielectric layer on the barrier layer;
removing a portion of the first dielectric layer for

91